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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 3

Complete if Known

Application Number	09/955,547
Filing Date	September 17, 2001
First Named Inventor	Mark Greenberg
Art Unit	2133
Examiner Name	Phung M. Chung
Attorney Docket Number	4259P076

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.†	Document Number	Publication Date Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code* (if known)			
me		US-4,015,238	03-29-1977	Davis	
		US-5,027,374	06-25-1991	Rossmann	
		US-5,349,608	09-20-1994	Graham, et al.	
		US-5,778,192	07-07-1998	Schuster, et al.	
		US-6,157,997	12-05-2000	Oowaki, et al.	
		US-6,189,126 B1	02-13-2001	Ulmer, et al.	
		US-6,266,687 B1	07-24-2001	Leyonhjelm, et al.	
me		US-6,301,314 B1	10-09-2001	Murayama	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.†	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code* - Number* - Kind Code* (if known)				
me	WO	00/74264 A1	12-07-2000	Martone		
me	EP	0 762 777 A2	03-12-1997	Uchiuni, et al.		

Examiner Signature		Date Considered	11/8/04
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					Examiner Name	Phung M. Chung
					Attorney Docket Number	4259P076

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
pme		HASHIDA MITSUYOSHI, "Hierarchical Network Management System and Control Method For Network Management Information" Patent Abstracts of Japan, Publication No. 07226777	
pme		MARIE-LAURE BOUCHERET, ET AL, "Fast Convolution Filter Banks For Satellite Payloads With On-Board Processing, IEEE Journal On Selected Areas in Communications, Vol. 17, No. 2, February 1999, pages 238-247.	
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pme		MICHAEL A. BREE, ET AL., "A Bit-Serial Architecture For A VLSI Viterbi Processor", Communications Systems Research Group, University of Saskatchewan, Saskatoon, IEEE, 1988, pages 72-77.	
pme		M.A. BREE, ETAL., "A Modular Bit-Serial Architecture For Large Constraint Length Viterbi Decoding", Communication Systems REsearch Group, University of Saskatchewan, Saskatoon, Canada, IEEE, 1990, pages 1501-1506.	
pme		MARC BIVER, ET AL., " Architectural Design and Realization Of A Single-Chip Viterbi Decoder", Elsevier Science Publishers B.V., Integration, the VLSI journal 8 (1989) October, No. 1, Amsterdam, NL, pages 3-16.	

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Sheet 3 of 3

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NON PATENT LITERATURE DOCUMENTS

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Pme		JAMES TSUI, "Frequency Channelization, Digital Techniques for Wideband Receivers", Second Edition, Pages 363-396, 2001 Artech House, Inc., Norwood, MA	
Pme		ZHENGDAO WANG and GEORGIOS B. GIANNAKIS, "Wireless Multicarrier Communications where Fourier Meets Shannon", Department of ECE, University of Minnesota, Minneapolis, MN., Pages 1-21.	
Pme		E. VERRIEST, ISEN, Implementing an Adaptive Noise Canceling System to enhance Sonar Receiver Performance Using the TMS320C31 DSP, ESIEE, Paris, September 1996, Texas Instruments, Pages 1-24.	
Pme		GA. SHAW, RA. FORD, J.C. ANDERSON, B.W. ZUERDNORFER, AH. NADERSON, RASSP Benchmark 2 Technical Description, Massachusetts Institute Of Technology Lincoln Library, 153 pages total.	

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Examiner Name	Chung, Phung M.
Attorney Docket Number	004259.P076

Sheet	1	of
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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		WO 01/59937 A2	8/16/2001	Qualcomm Inc.		

Date Considered

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			Filing Date	September 17, 2001
			First Named Inventor:	Mark Greenberg
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		BOUCHERET/MORTENSEN/FAVARO, "Fast Convolution Filter Banks for Satellite Payloads with On-Board Processing," IEE Journal on Selected Areas in Communications, February 1999, pp. 238-247, Vol. 17, No. 2, XP-000851758		

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